

The diagram shows a circuit with two input transistors, M100 and M200, and two output transistors, M300 and M400. A current source I₀ is connected to the gates of M100 and M200. The gates of M300 and M400 are connected to the drains of M100 and M200, respectively. The drains of M300 and M400 are connected to a feedback loop consisting of two transistors, J1 and J2, and a current source I₁. The output of the feedback loop is V_{REF}.

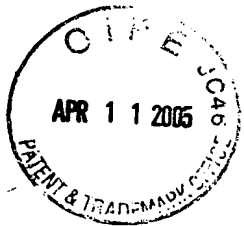


FIG . 2

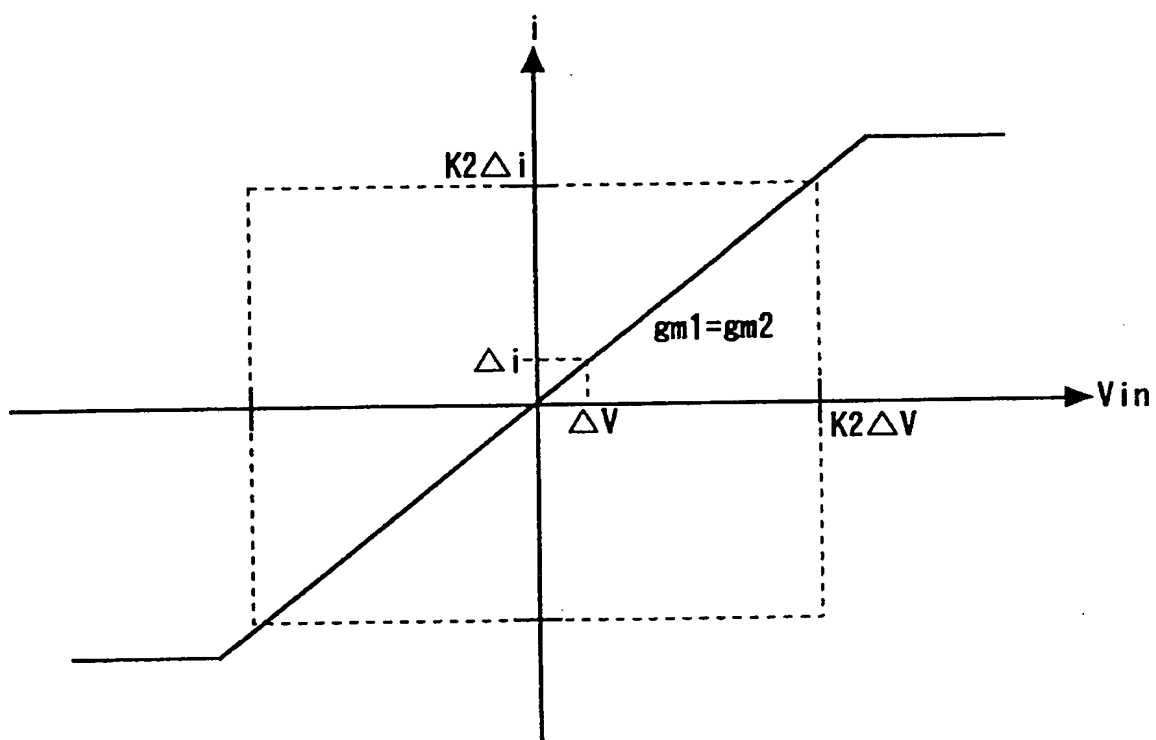




FIG . 3

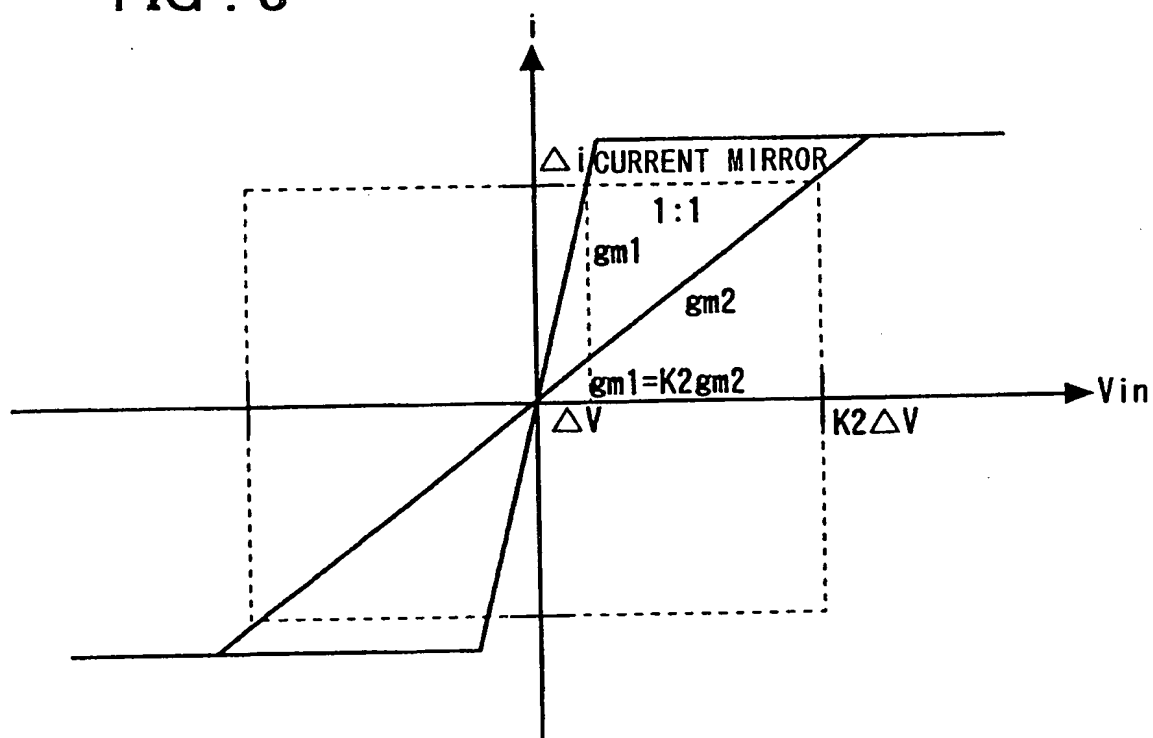


FIG . 4

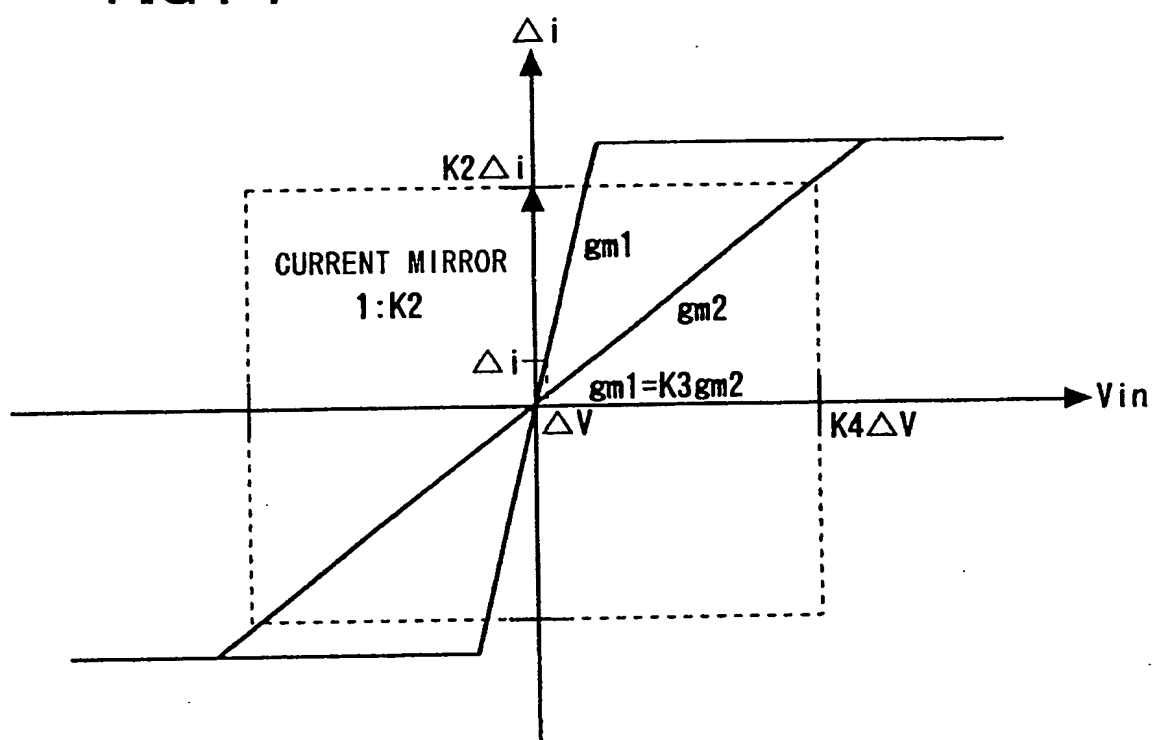


FIG . 5

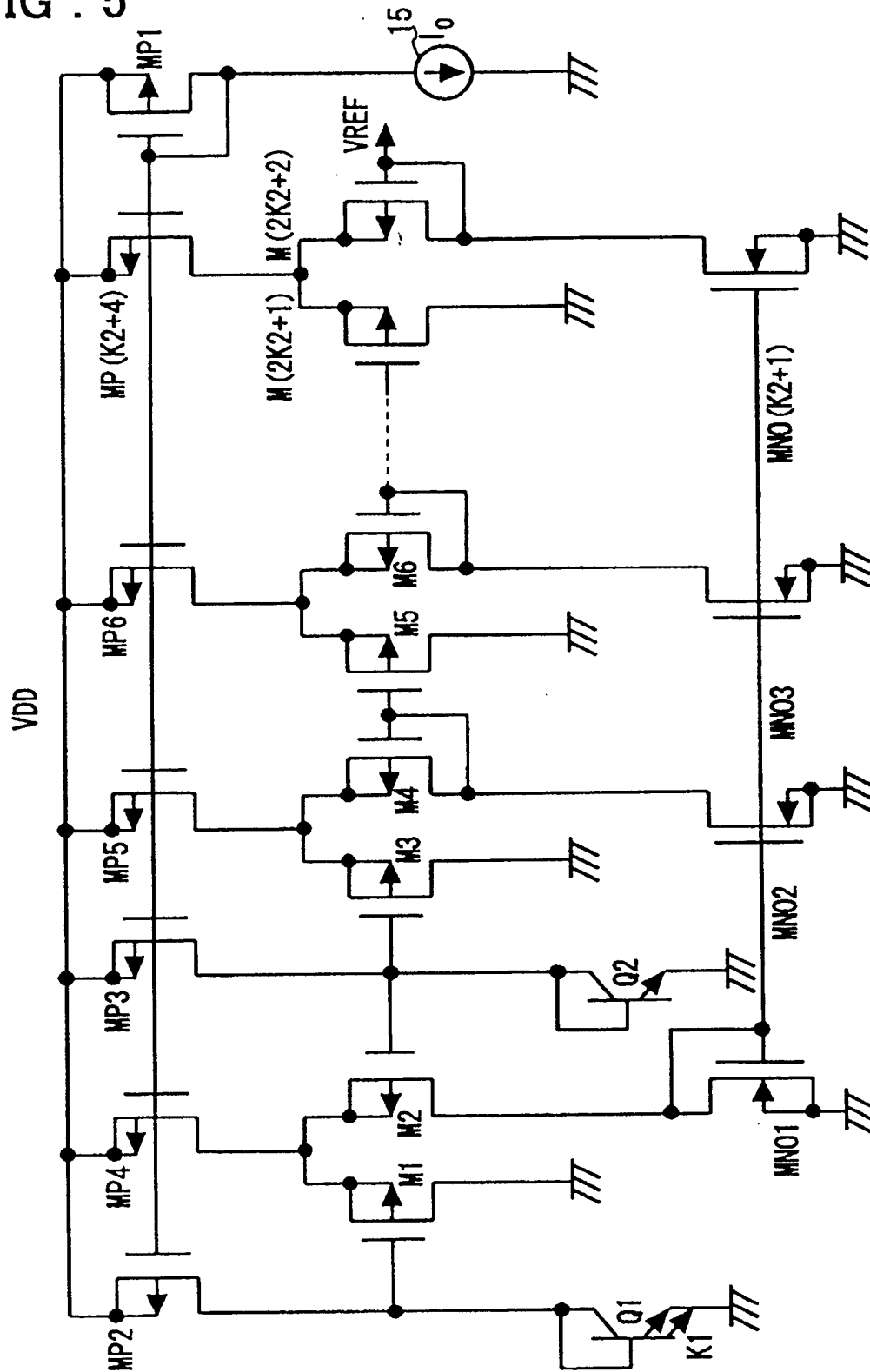


FIG. 6

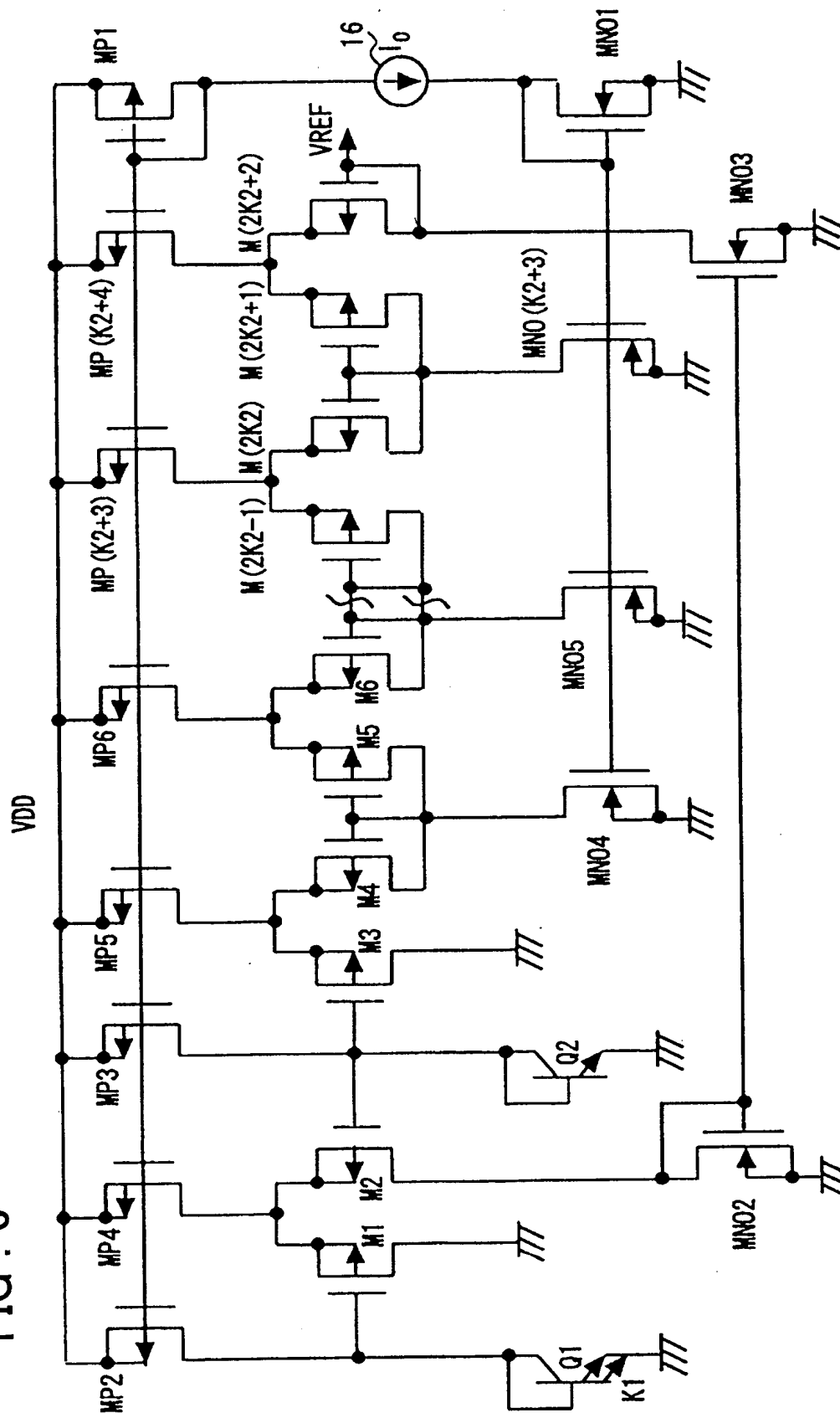


FIG . 7

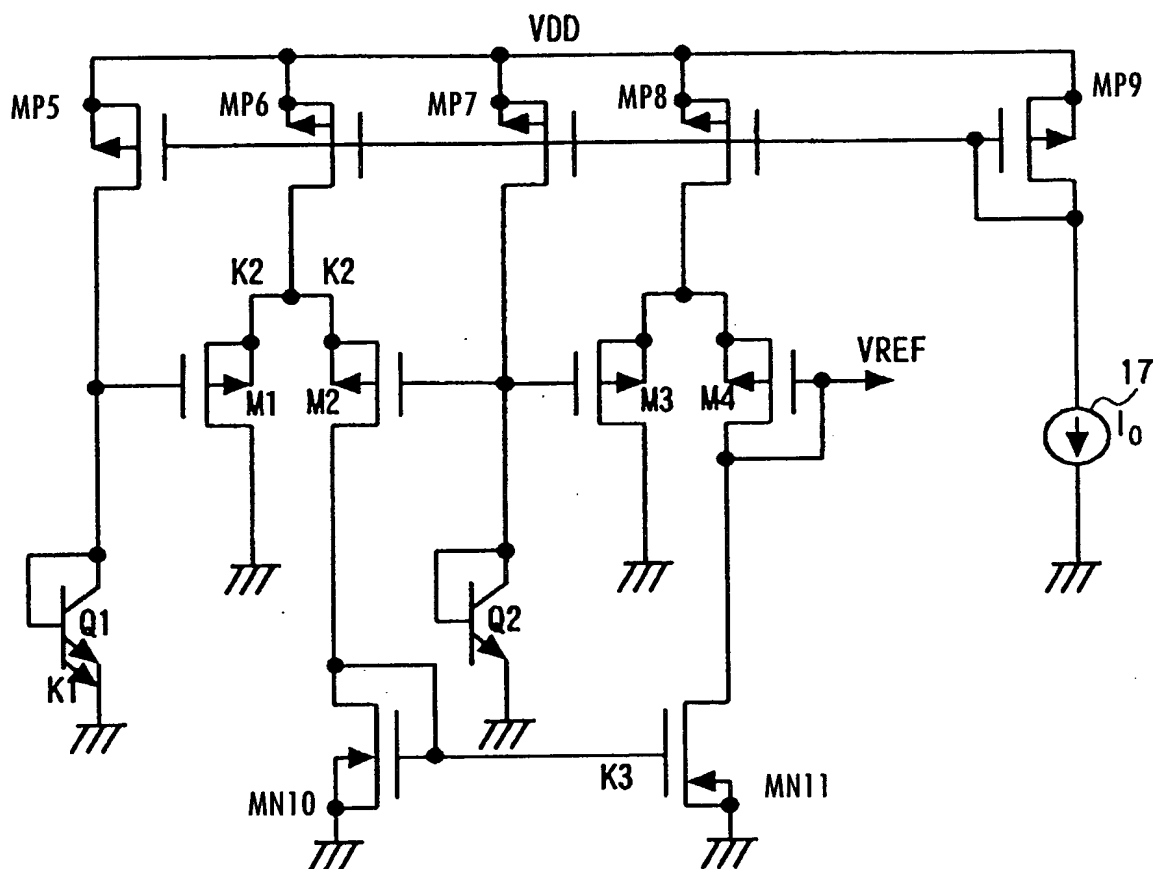


FIG. 8

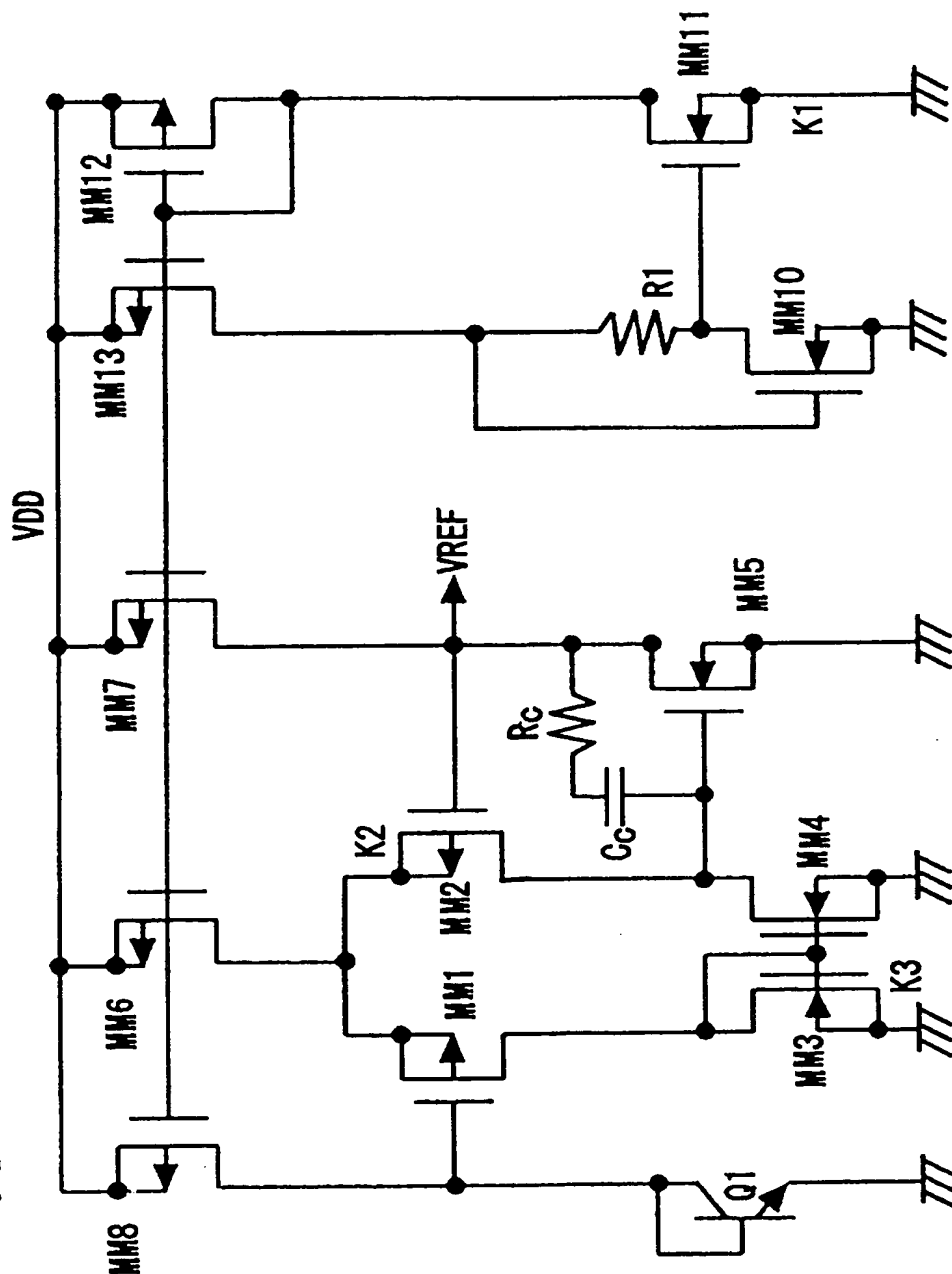


FIG . 10 PRIOR ART

